



# Test Report: NTU-1200-248

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1200W High Reliable True Sine Wave with UPS DC-AC Power Inverter

- **DESIGN VERIFY TEST**
  - Output Function Test
  - Input Function Test
  - Protection Function Test
  - Control Function Test
  - APPLICATION Test
  - Component Stress Test
- **SAFETY & E.M.C. TEST**
  - Safety Test
  - E.M.C. Test
- **RELIABILITY TEST**
  - ENVIRONMENT TEST

DESIGN VERIFY TEST

OUTPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	RATED POWER	1200W	IP: 48VDC Ta:25°C	<u>1224</u> W
2	MAXIMUM OUTPUT POWER (TYP)	(1)1380W/180sec. (2)1800w/10sec (3)SURGE POWER 2000W FOR 30CYCLE Vin (30 ± 5 CYCLE)	IP: 50VDC OP:TESTING LOAD Ta:25°C	(1) 228.8V/ 5.81 A/ 180.12 Sec (2) 228.6V/ 7.76 A/ 10.10 Sec (3) 225.6V/ 8.56 A/ 28 Cycle

CH3:O/P VAC CH4:O/P IAC

Fig1

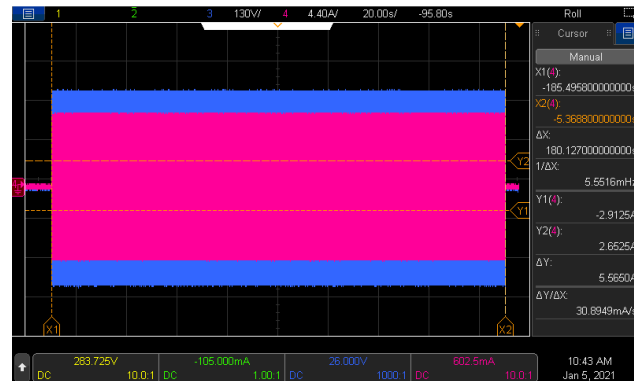


Fig2

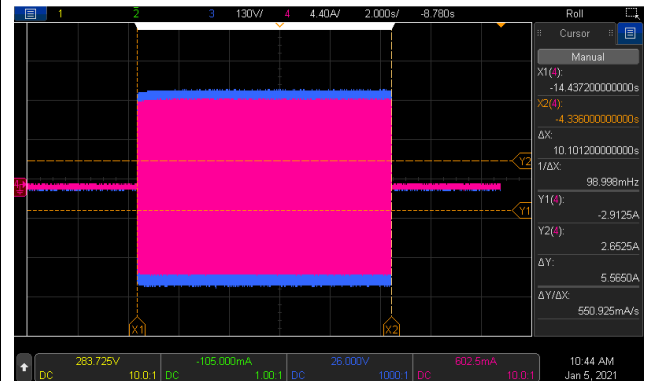
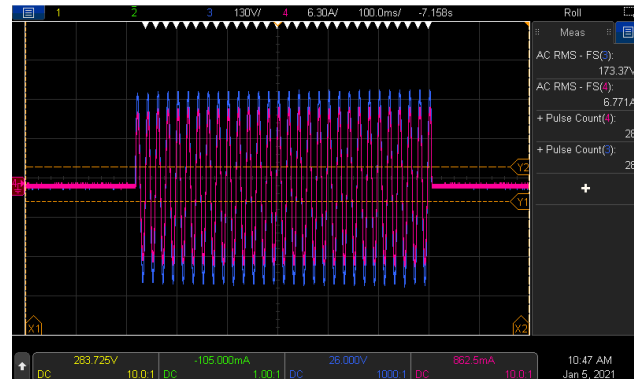









































Fig3



3	AC Voltage	200 / 220 / 230 / 240Vac selectable by DIP S.W	IP: 48VDC OP: FULL LOAD Ta:25°C	DIP S.W 200VAC: <u>199.28</u> V DIP S.W 220VAC: <u>219.86</u> V DIP S.W 230VAC: <u>229.02</u> V DIP S.W 240VAC: <u>238.92</u> V
4	FREQUENCY	50/60Hz (±0.1HZ) selectable by DIP S.W	IP: 48VDC OP: FULL LOAD Ta:25°C	DIP S.W 50HZ: <u>50.041</u> HZ DIP S.W 60HZ: <u>59.959</u> HZ

5	WAVEFORM	True sine wave (THD < 3%)	IP: 50VDC OP: 75% LOAD (900W) (1) Vo(min) (2) Vo(nor) (3) Vo(max) Ta: 25°C	(1) 1.45% / Vo(min) / 75% LOAD (2) 1.42% / Vo(nor) / 75% LOAD (3) 1.41% / Vo(max) / 75% LOAD
CH3:O/P VAC CH4:O/P IAC				
6	AC REGULATION	±3%	IP: 50VDC OP: 75% LOAD (900W) Ta: 25°C	-0.33 %
7	Overshoot /Undershoot	< ±10%	IP: 48VDC OP: (1) full load turn on (2) no load turn on (3) full /no load change Ta: 25°C	(1) -7.3 % (2) -6.52 % (3) -4.52 %
8	O/P voltage DC offset	Vin(nor) = 48 v · Vo < 200mV · no load : 76.7 mV / full load: 86.8 mV		

9	LED STATUS	<ul style="list-style-type: none"> <li> <b>Status test</b> <table border="1" data-bbox="424 293 1437 633"> <thead> <tr> <th>LED</th> <th>Status</th> <th>RESULT</th> </tr> </thead> <tbody> <tr> <td>Green</td> <td> Inverter OK</td> <td>OK</td> </tr> <tr> <td>Orange</td> <td> Remote off  Saving mode</td> <td>OK</td> </tr> <tr> <td>Red</td> <td> Abnormal Status (See SPEC)</td> <td>OK</td> </tr> </tbody> </table> </li> <li> <b>Battery test</b> <table border="1" data-bbox="424 707 1437 981"> <thead> <tr> <th>LED</th> <th>Battery RANGE</th> <th>RESULT</th> </tr> </thead> <tbody> <tr> <td><b>Green</b> </td> <td>50.0~62.0 Vdc±1v</td> <td>50.33Vdc ~ 62.22 Vdc</td> </tr> <tr> <td><b>Orange</b> </td> <td>44.0~50.0Vdc ±1v</td> <td>44.25Vdc ~ 50.31Vdc</td> </tr> <tr> <td><b>Red</b> </td> <td>&lt;44.0 Vdc ±1v &gt; 62.0vdc±1v</td> <td>&lt; 44.17Vdc &gt; 62.36 Vdc</td> </tr> </tbody> </table> </li> <li> <b>Load test</b> <table border="1" data-bbox="424 1055 1437 1328"> <thead> <tr> <th>LED</th> <th>LOAD RANGE</th> <th>RESULT</th> </tr> </thead> <tbody> <tr> <td><b>Green</b> </td> <td>Min. load ~ 40%±5% LOAD</td> <td>Min. load ~ 40.58%</td> </tr> <tr> <td><b>Orange</b> </td> <td>40%±5% ~ 80%±5% LOAD</td> <td>40.67% ~ 79.42%</td> </tr> <tr> <td><b>Red</b> </td> <td>≥ 80%±5% LOAD</td> <td>≥ 79.58 %</td> </tr> </tbody> </table> </li> <li> <b>AC Input</b> <table border="1" data-bbox="424 1402 1437 1664"> <thead> <tr> <th>LED</th> <th>AC INPUT</th> <th>RESULT</th> </tr> </thead> <tbody> <tr> <td><b>Green</b> </td> <td>Utility OK</td> <td>OK</td> </tr> <tr> <td><b>Green</b> </td> <td>Utility error</td> <td>OK</td> </tr> <tr> <td><b>Colorless</b> </td> <td>Utility disconnected</td> <td>OK</td> </tr> </tbody> </table> </li> </ul>	LED	Status	RESULT	Green	 Inverter OK	OK	Orange	 Remote off  Saving mode	OK	Red	 Abnormal Status (See SPEC)	OK	LED	Battery RANGE	RESULT	<b>Green</b> 	50.0~62.0 Vdc±1v	50.33Vdc ~ 62.22 Vdc	<b>Orange</b> 	44.0~50.0Vdc ±1v	44.25Vdc ~ 50.31Vdc	<b>Red</b> 	<44.0 Vdc ±1v > 62.0vdc±1v	< 44.17Vdc > 62.36 Vdc	LED	LOAD RANGE	RESULT	<b>Green</b> 	Min. load ~ 40%±5% LOAD	Min. load ~ 40.58%	<b>Orange</b> 	40%±5% ~ 80%±5% LOAD	40.67% ~ 79.42%	<b>Red</b> 	≥ 80%±5% LOAD	≥ 79.58 %	LED	AC INPUT	RESULT	<b>Green</b> 	Utility OK	OK	<b>Green</b> 	Utility error	OK	<b>Colorless</b> 	Utility disconnected	OK
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INPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	VOLTAGE RANGE (TYP)	40VDC~66VDC	IP: TESTING OP:NO LOAD/FULL LOAD Ta:25°C	<u>40.32</u> VDC~ <u>66.23</u> VDC/NO LOAD <u>40.31</u> VDC~ <u>66.18</u> VDC/FULL LOAD
			I/P: LOW-LINE=42V HIGH-LINE=65V O/P:FULL/MIN LOAD (PLEASE CHECK DERATING CURVE) ON:30Sec OFF:30Sec 10MIN (POWER ON/OFF NO DAMAGE) I/P: 48V O/P:FULL LOAD ON:30ec OFF:30ec 12Hr (POWER ON/OFF NO DAMAGE)	Test: <u>OK</u>
2	DC CURRENT (TYP)	30A	IP: 48VDC OP:FULL LOAD Ta:25°C	<u>27.16</u> A
3	NO LOAD DISSIPATION (Typ.)	≤ 8W@standby saving mode ≤ 25W@NON-Saving Mode	IP: 48VDC OP:NO LOAD Ta:25°C	<u>5.61</u> W <u>24.00</u> W
4	SAVING MODE TO NORMAL	Po ≥ 25W	IP: 48VDC OP: TESTING LOAD Ta:25°C	<u>≥ 19</u> W
5	NORMAL TO SAVING MODE	Po ≤ 10W	IP: 48VDC OP: TESTING LOAD Ta:25°C	<u>≤ 11</u> W
6	OFF MODE CURRENT DRAW (Typ.)	≤ 1mA	IP: 48VDC OP: Sw off Ta:25°C	<u>0.81</u> mA
7	EFFICIENCY(TYP)	900W/93%	IP: 50VDC OP: Po=900W 230V/50HZ Ta:25°C	<u>93.4</u> %

AC UPS MODE

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT		
1	AC Taper Voltage RANGE	AC input high / low line limit:No Load				
		AC Voltage	limit	Voltage RANGE	RESULT	
		230V	High limit (To INV mode)	Vac >230V +16%±3%	263.67 V	
			Recovery to high (To AC mode)	Vac <230V+13%±3%	258.38 V	
			Low limit (To INV mode)	Vac <230V-16%±3%	194.78 V	
			Recovery to low (To AC mode)	Vac >230V-13%±3%	198.92 V	
		200V	High limit (To INV mode)	Vac >200V+16%±3%	227.15 V	
			Recovery to high (To AC mode)	Vac <200V+13%±3%	221.61 V	
			Low limit (To INV mode)	Vac <200V-16%±3%	168.27 V	
			Recovery to low (To AC mode)	Vac >200V-13%±3%	174.41 V	
		220V	High limit (To INV mode)	Vac >220V+16%±3%	251.58 V	
			Recovery to high (To AC mode)	Vac <220V+13%±3%	245.98 V	
			Low limit (To INV mode)	Vac <220V-16%±3%	186.49 V	
			Recovery to low (To AC mode)	Vac >220V-13%±3%	190.23 V	
		240V	High limit (To INV mode)	Vac >240V+16%±3%	272.15 V	
			Recovery to high (To AC mode)	Vac <240V+13%±3%	270.24 V	
			Low limit (To INV mode)	Vac <240V-16%±3%	205.39 V	
			Recovery to low (To AC mode)	Vac >240V-13%±3%	209.37 V	
		2	FREQUENCY RANGE	45 ~ 65Hz	IP:48VDC OP: FULL LOAD Ta:25°C	TEST: <u>OK</u>
		3	TRANSFER TIME (TYP)	t<10ms±3ms inverter→by pass	IP: 48VDC OP: (1) no load (2) full load Ta:25°C	(1) no load a. INTER→BY PASS <u>2</u> ms b. BY PASS-INVERTER <u>10.7</u> ms (2) full load c. INTER→BY PASS <u>4.62</u> ms d. BY PASS-INVERTER <u>12.8</u> ms

PROTECTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	BAT LOW ALARM	44V±1VDC	IP: TESTING OP:FULL LOAD SW:ON Ta:25°C	<u>44.108</u> V
2	BAT LOW SHUT DOWN	40V±1VDC	IP: TESTING OP: FULL LOAD SW:ON Ta:25°C	<u>40.277</u> V

3	BAT LOW RESTART	50V±1VDC	IP: TESTING OP: FULL LOAD SW:ON Ta:25°C	<u>50.258</u> V
4	BAT HIGH ALARM	62V±1VDC	IP: TESTING OP:FULL LOAD SW:ON Ta:25°C	<u>62.33</u> V
5	BAT HIGH SHUT DOWN	66V±1VDC	IP: TESTING OP: FULL LOAD SW:ON Ta:25°C	<u>66.3</u> V
6	BAT HIGH RESTART	60V±1VDC	IP: TESTING OP: FULL LOAD SW:ON Ta:25°C	<u>60.32</u> V
7	OVER TEMPERATURE	Shut down o/p voltage: re-power on	IP: HI LINE/LOW-LINE OP: FULL LOAD SW:ON Ta:25°C	Shut down o/p voltage, re-power on to recover LED DISPLAY: <u>OK</u>
8	OUTPUT SHORT	Shut down o/p voltage: re-power on	IP: 48VDC O/P: FULL LOAD SW:ON Ta:25°C	Shut down o/p voltage, re-power on to recover LED DISPLAY: <u>OK</u> (1).TEST: <u>OK</u>
10	OVER LOAD (typ.)	105%~115%LOAD 180sec 115%~150%LOAD 10 sec Shut down o/p voltage, re-power on to recover	IP: 48VDC OP: TESTING SW:ON Ta:25°C	(1). <u>104.7 %~113.7 %</u> <u>180.12</u> sec (2). <u>114.9%~ 147.7 %</u> <u>10.10</u> sec Shut down o/p voltage, re-power on to recover

**CONTROL FUNCTION TEST**

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	REMOTE CONTROL	(1).Power ON-OFF remote control by front panel dry contact connector (by RELAY) Open : Normal work Short : Remote off (2). IRC3	IP: 48VDC OP: FULL LOAD Ta:25°C	Open : Normal work Short : Remote off (1).TEST: <u>OK</u> (2).TEST: <u>OK</u>

**APPLICATION TEST**

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	LAMP	LAMP: <u>619</u> W · turn on <u>OK</u> LAMP: <u>1235</u> W · turn on <u>OK</u> LAMP: <u>1432</u> W · turn on <u>OK</u>	1. Vin=HIGH LINE 2. O/P=110V/60Hz TEST: <u>OK</u>	
2	INDUCTION MOTOR	<u>0.5</u> HP	1. Vin=HIGH LINE 2. O/P=110V/60Hz TEST: <u>OK</u>	

3	SWITCHING POWER SUPPLY	WITH PFC: <u>RSP-1600-48</u> O/P= <u>1189</u> W	1. Vin=HIGH LINE 2. O/P=110V/60Hz TEST: <u>OK</u>
		NO PFC: <u>SE-1000-48</u> O/P= <u>457</u> W	1. Vin=HIGH LINE 2. O/P=110V/60Hz TEST: <u>OK</u>

**COMPONENT WEAFORM TEST**

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	DC TO DC Power Transistor ( D to S) or (C to E) Peak Voltage	Q101/ Q105 Rated : 200V /65 A	I/P: high line O/P:V(max)/Freq 60HZ VDS: O/P: (1)Full Load Turn On (2) Output Short (3)O.L.P(200%) Turn On (4) NO LOAD Turn On (5) Saving mode Ta:25°C	Q101 Q105 (1) 161V (1)165V (2) 163V (2)163V (3) 161V (3)163V (4) 165V (4)170V (5) 165V (5)165V
2	DC TO DC Diode Peak Voltage	D 151 Rated :600V/ 20A	I/P: high line O/P:V(max) /Freq 60HZ O/P: (1)Full Load Turn On (2) Output Short (3)O.L.P(200%) Turn On (4) NO LOAD Turn On (5) Saving mode Ta:25°C	D151 D152 (1) 538V (1) 534V (2) 587V (2) 546V (3) 583V (3) 546V (4) 579V (4) 555V (5) 563V (5) 546V
3	DC BUS Capacitor Voltage	C161/C162 Rated : 680 u/ 315 V	I/P: high line O/P:V(max) /Freq 60HZ O/P: (1)Full Load Turn On (2) Output Short (3)O.L.P(200%) Turn On (4) NO LOAD Turn On (5) Saving mode Ta:25°C	C161 C162 (1) 257V (1) 265V (2) 261V (2) 269V (3) 261V (3) 273V (4) 269V (4) 269V (5) 257V (5) 265V
4	DC TO AC Power Transistor ( D to S) or (C to E) Peak Voltage	Q 1 Rated : 30A / 650 V	I/P: high line O/P:V(min)/Freq 50HZ VDS: O/P: (1)Full Load Turn On (2) Output Short (3)O.L.P(200%) Turn On (4) NO LOAD Turn On (5) Saving mode Ta:25°C	Q1 Q3 (1)555V (1)534V (2)571V (2) 571V (3)555V (3) 542V (4)555V (4) 555V (5)551V (5) 555V



5	AUX PWM MOS	Q201 Rated : 65 A/ 200 V  Q501 Rated : 65 A/ 200 V	I/P: high line O/P:V(max) /Freq 60HZ O/P: (1)Full Load Turn On (2) Output Short (3)O.L.P(200%) Turn On (4) NO LOAD Turn On (5) Saving mode Ta:25°C	Q201 (1) 170V (2) 170V (3) 170V (4) 170V (5) 170V	Q501 (1) 49.0V (2) 49.0V (3) 49.0V (4) 49.0V (5) 49.0V
6	Control IC Voltage Test	MCU IC U301 Rated 2.4 V~ 3.6 V  AUX IC U201 Rated 8.2V~30V  CHARGE IC U501 Rated -0.3V~20V  Gate Driver IC U81 Rated -0.3V~20V	I/P: high line O/P:V(max) /Freq 60HZ O/P: (1)Full Load Turn On (2) Output Short (3)O.L.P(200%) Turn On (4) NO LOAD Turn On (5) Saving mode Ta:25°C	U301 (1) 3.41V (2) 3.37V (3) 3.41V (4) 3.41V (5) 3.37V  U201 (1) 12.12V (2) 12.12V (3) 12.12V (4) 12.12V (5) 12.12V	U501 (1) 12.36V (2) 12.36V (3) 12.36V (4) 12.36V (5) 12.36V  U81 (1) 5.06V (2) 5.06V (3) 5.06V (4) 5.06V (5) 5.06V

**SAFETY TEST**

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	WITHSTAND VOLTAGE	BAT I/P-AC I/P: 3 KVAC/min BAT I/P-AC O/P: 3 KVAC/min AC O/P-FG: 1.5 KVAC/min	BAT I/P-AC I/P: 3.6 KVAC/min BAT I/P-AC O/P: 3.6 KVAC/min AC O/P-FG:1.8 KVAC/min Ta:25°C	BAT I/P-AC I/P : 7.17 mA BAT I/P-AC O/P: 7.08 mA AC O/P-FG: 5.79 mA NO DAMAGE
2	GROUNDING CONTINUITY	IEC62368 FG(PE) TO CHASSIS OR TRACE < 100 mΩ	40 A / 2min Ta:25°C	3mΩ

**E.M.C TEST**

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	RADIATION	EN55032 CISPR32 (expect for Type-UN) CLASS A	I/P: 48 VDC O/P: :FULL/50% LOAD Ta:25°C	CLASS A
2	E.S.D	EN61000-4-2 AIR : 8KV / Contact : 4KV	I/P: 48VDC O/P:FULL LOAD Ta:25°C	PASS
3	HARMONIC	EN61000-3-2 CLASS A	I/P:230VAC/50HZ O/P:FULL LOAD Ta:25°C	PASS
4	CONDUCTION	EN55032 CISPR32 (expect for Type-UN) CLASS A	I/P : 230 VAC (50HZ) O/P : FULL/50% LOAD Ta : 25°C	PASS
5	E.F.T	EN61000-4-4 INPUT : 1KV	I/P : 230 VAC/50HZ O/P : FULL LOAD Ta : 25°C	CRITERIA A
6	SURGE	IEC61000-4-5 L-N : 1KV	I/P : 230 VAC/50HZ O/P : FULL LOAD	CRITERIA A



2	LOW TEMPERATURE TURN ON TEST	TURN ON AFTER 2 HOUR	I/P : 50VDC O/P : 100%LOAD Ta= -25 °C	TEST : OK
3	HIGH HUMIDITY HIGH TEMPERATURE HIGH VOLTAGE TURN ON TEST	AFTER 12 HOURS IN CHAMBER ON CONTROL 35 °C NO DAMAGE	I/P : 65VDC O/P : FULL LOAD Ta= 35 °C HUMIDITY= 95 %R.H	TEST : OK
4	STORAGE TEMPERATURE TEST	1. Thermal shock Temperature : -45°C~ +90°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle : 5 CYCLE 5. Input/Output condition : STATIC		TEST : OK
5	THERMAL SHOCK TEST	1. Thermal shock Temperature : -25°C~ +40°C 2. Temperature change rate : 25°C / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle : 10 CYCLE 5. Input/Output condition : 50VDC/Full Load		TEST : OK
6	VIBRATION TEST	1 Carton & 1 Set (1) Waveform : Sine Wave (2) Frequency : 10~500Hz (3) Sweep Time : 10min/sweep cycle (4) Acceleration : 4G (5) Test Time : 60min in each axis (X.Y.Z) (6) Ta : 25°C		TEST : OK
7	CAPACITOR LIFE CYCLE	SUPPOSE C101 IS THE MOST CRITICAL COMPONENT (1) I/P : 50VDC O/P : FULL LOAD Ta= 25 °C LIFE TIME (2) I/P : 50VDC O/P : FULL LOAD Ta= 35 °C LIFE TIME		(1) 290924.7HRS (2) 191938.7HRS
8	MTBF	Conducted by Parts Stress Analysis Prediction 460.5K hrs min. Telcordia TR/SR-332 (Bellcore) ; 58.3K hrs min. MIL-HDBK-217F (25°C)		
9	Ongoing Reliability Test	I/P : 50VDC O/P : 80% LOAD TA=50°C Demonstration Mean Time Between Failure : 30,000 hours		

TEST RESULT	TESTER	REVIEW	APPROVAL
PASS	LIUTT		WANGDZ

2018.4.30 GP-A50-F010